

LSI Logic Corporation  
1621 Barber Lane  
M/S AD-106  
Milpitas, CA 95035



RECEIVED  
CENTRAL FAX CENTER

JUN 01 2006

# Fax

<b>To:</b>	Nghia M. Doan	<b>From:</b>	Peter P. Scott
<b>Fax:</b>	571.273.5973 , 571-273-8300	<b>Pages:</b>	25
<b>Phone:</b>	571.272.5973	<b>Date:</b>	June 1, 2006

Appl. No.	:	10/697,357
Applicant	:	Viswanathan Lakshmanan
Filed	:	October 29, 2003
TC/A.U.	:	2825
Examiner	:	Nghia M. Doan
Docket No.	:	03-1772
Customer No.	:	24319

### Request for Drawing Correction Approval

Attached to this cover sheet is Applicant's Request for Drawing Correction Approval.

The information contained in this facsimile transmission is intended only for the personal and confidential use of the recipient(s) named above. This transmission may be an attorney-client communication and/or work product and as such is privileged and confidential. If the reader of this transmission is not the intended recipient or an agent responsible for delivering it to the intended recipient, you are hereby notified that you have received this transmission in error and that any review, dissemination, distribution, or copying of this transmission is strictly prohibited. If you have received this transmission in error, please notify the sender immediately, and delete the original transmission.

RECEIVED  
CENTRAL FAX CENTER

JUN 01 2006

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Viswanathan Lakshmanan  
Serial No.: 10/697,357  
Filed: October 29, 2003  
For: Method Of Partitioning An  
Integrated Circuit Design For  
Physical Design Verification

Group Art Unit: 2825  
Attorney Docket No: 03-1772

**Certificate of Transmission**

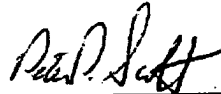
I hereby certify that this correspondence is being facsimile transmitted to the following Patent and Trademark Office  
Fax number:

571.273.8300

on

6/1/2006

(Date).



Peter P. Scott

**REQUEST FOR DRAWING CORRECTION APPROVAL**

Mail Stop Amendment  
Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

Attention: Nghia M. Doan  
Examiner  
571.272. 5973

Dear Sir:

The Applicant respectfully submits a copy of a proposed correction to FIG. 3 for review  
and approval by the Examiner.

03-1772

## **RESPONSE**

The Examiner requested the Applicant to submit a proposed correction to FIG. 3 to address the repetition of the text in steps 314 and 316.

In response the Applicant is submitting a proposed revision to FIG. 3, wherein the text of step 316 has been replaced with text taken from the specification in connection with step 316 ("Assign each filtered data deck to a separate processor queue according to memory size").

Approval of the proposed correction is requested. Upon receipt of Examiner's approval Applicant will submit a replacement sheet incorporating the approved changes.

If it is felt for any reason that direct communication with the Applicant's attorney would serve to advance prosecution of this case to finality, the Examiner is invited to call the undersigned Peter P. Scott at the telephone number below.

**AUTHORIZATION TO PAY AND PETITION  
FOR THE ACCEPTANCE OF ANY NECESSARY FEES**

If any charges or fees must be paid in connection with this communication (including but not limited to the payment of an extension fee or issue fees), or if any overpayment is to be refunded in connection with the present application, any such charges or fee, or any such overpayment may be respectively paid out of, or into, the Deposit Account No. 12-2252 of LSI Logic Corporation. If any such payment also requires a Petition or Extension Request, please construe this authorization as the required Petition or Request.

Respectfully submitted,

**Viswanathan Lakshmanan**

By: \_\_\_\_\_



Peter P. Scott  
Attorney for Applicant  
Reg. No. 33,279

LSI Logic Corporation  
1621 Barber Lane  
M/S AD-106  
Milpitas, CA 95035

719.533.7969 (Voice)  
719.533.7254 (Fax)

Date: \_\_\_\_\_

